



I/O Register (Auszug) / register set (essentials)

Table of I/O registers for the AVR microcontroller. Columns include Name, Bit 7-0, and various register fields like UCSRnC, OCRRn, TCCRn, etc.

Befehlssatz (Auszug) / instruction set (essentials)

Table of AVR instructions. Columns include Mn, Op, Description, Operation, Flags, and CLK. Categories include BRANCH INSTRUCTIONS, BIT AND BIT-TEST INSTRUCTIONS, and MCU CONTROL INSTRUCTIONS.

Table of Arithmetic and Logic Instructions (ARITHMETIC AND LOGIC INSTRUCTIONS). Columns include Mn, Op, Operation, and Flags. Examples include ADD, ADC, SUB, MUL, etc.

Table of Data Transfer Instructions (DATA TRANSFER INSTRUCTIONS). Columns include Mn, Op, Operation, and Flags. Examples include MOV, LDD, ST, LPM, etc.

