

Legende:
 von Peripherie genutzte Pins (red)
 frei verfügbare Pins (green)
 System-Pins (black)

Board-Connector

1 = P3.00	13 = 3/7.5V
2 = P3.02	14 = GND
3 = P3.03	15 = P14.0
4 = P3.04	16 = P14.1
5 = P1.11	17 = P14.2
6 = P1.12	18 = P14.3
7 = P1.13	19 = P14.4
8 = P0.02	20 = P14.5
9 = P0.03	
10 = P0.04	
11 = P0.05	
12 = P0.06	

Pin Lists:

- VDD5:** P0.10, P0.9, P3.1, HIB_1, P15.3, P14.15, P14.13, P14.7, P14.5, P14.3, P14.1, VAREF, P2.15, P2.14, P2.1, GND, GND, GND
- VDD3.3:** P0.0, P0.1, P3.2, P3.0, P15.2, P14.14, P14.12, P14.6, P14.4, P14.0, P14.9, P14.8, P14.0, RST#, GND, GND, GND
- LED:** P1.0, P1.1
- Taster:** P1.15, P1.14
- Micro-SD:** 4.0, 4.1, 1.6, 1.7, 3.5, 3.6
- USB:** USB_D-, USB_D+, VBUS
- myUSBtoUART:** Rx P2.15, Tx P2.14
- ext. Flash:** P1.2-P1.5, P1.8, P1.10
- Ethernet PHY:** P2.7-P2.9, P2.2-P2.5, P2.0, P15.9, P15.8

GPIO Pin Mapping - Externe Interrupts

	ERU0				ERU1			
	ETL0	ETL1	ETL2	ETL3	ETL0	ETL1	ETL2	ETL3
A0	P0.1	P0.10	P1.5	P1.1	P1.5	P1.15	P1.3	P0.5
A1	P3.2		P0.8	P3.6				
A2	P2.5	P2.3	P0.13	P0.11				
A3								
B0	P0.0	P0.9	P1.4	P1.0	P2.1	P2.7	P1.2	P0.3
B1	P3.1	RTCXTAL1	P0.7	P3.5				
B2	P2.4	P2.2	P0.12	P0.6				
B3	P2.0	P2.6	P0.4	P0.2				

ADC Pin Mapping

ADC	G0	G1	G2	G3
0	P14.0	P14.8	P14.4	P15.8
1	P14.1	P14.9	P14.5	P15.9
2	P14.2	P14.2	P15.2	P14.8
3	P14.3	P14.3	P15.3	P14.9
4	P14.4	P14.12	P15.4	P15.12
5	P14.5	P14.13	P15.5	P15.13
6	P14.6	P14.14	P15.6	P15.14
7	P14.7	P14.15	P15.7	P15.15

PWM Pin Matrix CCU8x

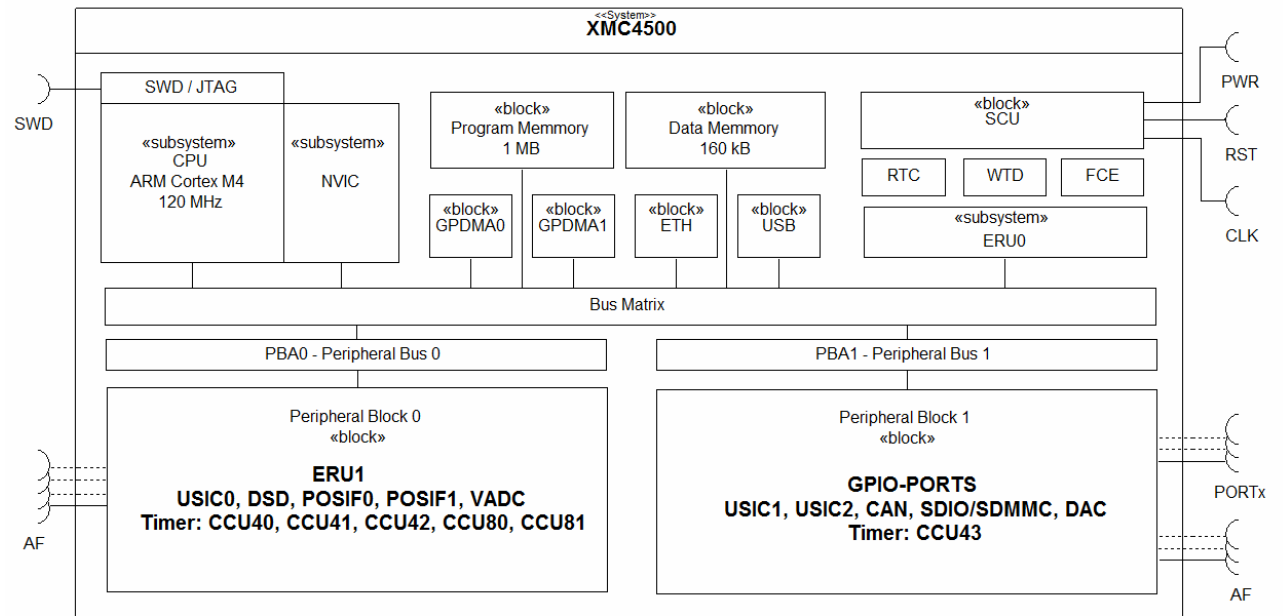
	CCU80		CCU81	
	OUT0	OUT1	OUT2	OUT3
OUT0	P0.5	P5.11	P1.15	
OUT01	P0.2	P5.8	P1.12	P2.2
OUT02	P0.10		P5.7	
OUT03	P2.7		P5.6	
OUT10	P0.4	P5.10	P1.5	P1.14
OUT11	P0.1	P2.15	P1.11	P2.1
OUT12	P0.9		P5.5	
OUT13	P2.6		P5.4	
OUT20	P0.3	P5.9	P1.4	P1.13
OUT21	P0.0	P2.14	P1.10	P2.0
OUT22	P2.9	P2.11	P5.3	
OUT23	P1.5		P5.2	
OUT30	P0.6		P6.1	
OUT31	P0.11		P6.0	
OUT32	P2.8		P5.1	
OUT33	P1.4		P5.5	P2.12

USIC UART Pin Mapping

		Tx			Rx				
U0	C0	P1.5	P1.7	P5.1	P1.5	P1.4	P4.7	P0.0	P5.0
	C1	P2.5	P3.13	P6.4		P2.5	P6.3	P3.13	P4.0
U1	C0	P0.5	P2.14		P0.4	P0.5	P2.14	P2.15	
	C1	P0.1	P3.15	P4.2	P2.2	P3.14	P4.2		
U2	C0	P5.0	P3.8		P5.1	P5.0	P3.7		
	C1	P3.11	P3.5		P3.5	P3.4	P4.0	P3.12	

PWM Pin Matrix CCU4x

	CCU40		CCU41		CCU42	
	OUT0	OUT1	OUT2	OUT3	OUT0	OUT1
OUT0	P1.3	P0.15	P2.5	P3.10	P3.0	P3.6
OUT1	P1.2	P0.14	P2.4	P3.9	P3.13	P3.5
OUT2	P1.1	P0.13	P2.3	P3.8	P3.12	P3.4
OUT3	P1.0	P0.12	P2.2	P3.7	P3.11	P3.3



Klassenübersicht myXMC C++ Framework 0.x (Auszug)

